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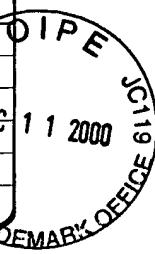
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PTO/SB/06A (10-96)

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| <p>Substitute for form 1449A/PTO</p> <h1>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h1> | | | | <p><i>Complete if Known</i></p> | |
| Sheet | 3 | of | 4 | Application Number | 09/473,575 |
| | | | | Filing Date | December 28, 1999 |
| | | | | First Named Inventor | Darrell D. Boggs |
| | | | | Group Art Unit | 2755-2127 |
| | | | | Examiner Name | Unknown ✓ O |
| | | | | Attorney Docket Number | 42390P6871 |

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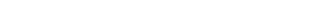
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| | | | | Examiner Name | Unknown ✓ 0 |
| | | | | Attorney Docket Number | 42390P6871 |

OTHER ART (Including Title, Author, Date, Pertinent Pages, etc.)

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| Examiner Signature |  | Date Considered | 9/3/03 |
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet 3 of 3

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| Filing Date | December 28, 1999 |
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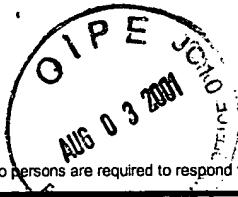
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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| Group Art Unit | 2755 2121 |
| Examiner Name | Unknown ✓ 0 |
| Attorney Docket Number | 42390P6871 |

OTHER ART (Including Title, Author, Date, Pertinent Pages, etc.)

| Exam. Initials | Cite No. | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. |
|----------------|----------|---|
| LV | | R. GURU PRASADH, ET AL., A Benchmark Evaluation of a Multi-Threaded RISC Processor Architecture, 1991, pp I84-I91, National Science Foundation and Cray Research Incorporated |
| | | ROBERT A. IANNUCCI, ET AL., Multithreaded Computer Architecture: A Summary of the State of the Art, pp 167-200, Kluwer Academic Publishers, Boston/London/Dordrecht |
| | | Proceedings, The 23rd Annual International Symposium on Computer Architecture, May 22-24, 1996, pp 191-202, ACM SIGARCH, IEEE Computer Society, TCCA, Philadelphia, Pennsylvania |
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| | | DENNIS LEE, ET AL. & BRAD CALDER, ET AL., Instruction Cache Fetch Policies for Speculative Execution, pp 1-11, Department of Computer Science |
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| | | PETER SONG, Multithreading Comes of Age, July 14, 1997, pp 13-18, Microdesign Resources |
| | | RUEDIGER R. ASCHE, Multithreading for Rookies, July 31, 1998, pp 1-15, Microsoft Developer Network Technology Group |
| ↓ | | IEEE COMPUTER SOCIETY TECHNICAL COMMITTEE ON COMPUTER ARCHITECTURE, Second International Symposium on High-Performance Computer Architecture, February 3-7, 1996, pp 291-301 |
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| Examiner Signature | T. Hansen | Date Considered | 9/3/03 |
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| | | | | Examiner Name | VO |
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| Sheet | 3 | of | 3 | | |

| | | | |
|-----------------------|--------------|--------------------|--------|
| Examiner Signature | <i>Wharn</i> | Date Considered | 9/3/03 |
|-----------------------|--------------|--------------------|--------|

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¹Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document, by the two-letter code (WIPO Standard S.3). ⁴For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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6